



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **Kenichi ATANABE et al.**

Group Art Unit: **2826**

Serial No.: **10/622,614**

Examiner: **Alexander O. Williams**

Filed: **July 21, 2003**

Confirmation No.: **2630**

For: **SEMICONDUCTOR DEVICE FOR PREVENTING DEFECTIVE
FILLING OR INTERCONNECTION AND CRACKING OF
INSULATING FILM**

Attorney Docket Number: **030877**

Customer Number: **38834**

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

December 2, 2005

Sir:

In compliance with 37 C.F.R. §1.56, Applicants direct the attention of the Patent and Trademark Office to the documents listed on the attached PTO/SB/08. A copy of each non-U.S. document is enclosed herewith.

Each listed document was cited in a communication from a foreign patent office. These documents are cited in the Notification of Reason(s) for Refusal mailed October 25, 2005 of the corresponding Japanese application in the present application. A copy of the Notification of Reason(s) for Refusal is also attached. Please note that one of the cited references, JP 11-54705A was previously filed with the Information Disclosure Statement on March 7, 2005.

Information Disclosure Statement
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If there are any fees due in connection with the filing of this paper, please charge Deposit
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Respectfully submitted,
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Enclosure: PTO/SB/08;
2 Documents with 2 English Abstracts; and
Notification of Reasons for Refusal